

## Senior Mixed-Signal IC Design Engineer

**Job Description:** Senseeker Corp is looking for a senior integrated circuit (IC) design engineer to join our IC design team and take on mixed-signal design of CMOS image sensors and related integrated circuits in 65 nm to 180 nm processes. Responsibilities will include digital and analog IC design, simulation, layout and verification, Verilog/Verilog-A behavioral modeling, contributing novel ideas to the company's IP portfolio, interfacing with customers, keeping up with the state-of-the art, and maintaining good written documentation. You will also help improve our EDA tool flow and efficiency. You should be comfortable working in a relaxed small business environment and understand how to get tasks accomplished independently when needed.

**Required Education and Experience:** MS in Electrical Engineering with at least 4 years of industry experience in IC design or PhD in Electrical Engineering with at least 2 years of industry experience in IC design

### Required Attributes:

- US person status **required** for this position, pursuant to the terms of government contracts with Senseeker\*
- CMOS integrated circuit design, simulation, and layout experience
- Knowledge of high-density arrayed CMOS low-noise analog and mixed-signal design techniques
- Verilog, Verilog-A/Verilog-AMS modeling
- Solid understanding of solid-state device physics
- Excellent teamwork skills, good written and verbal communication and a self-driven approach to work
- Previous tapeout experience is a must

### Desired Attributes:

- Practical design experience with digital converters (ADCs/DACs), PLL/DLL, SerDes, image sensors and digital readout ICs
- Proficient in image sensor characterization and noise analysis
- Comfortable with multiple CAD tools and foundry PDKs
- Experience with digital synthesis tools
- Experience with Siemens Tanner / Calibre design flow

**Job Type and Location:** Full-time in Santa Barbara, CA, USA

**Base Salary Range:** \$105k - \$180k per year

**Benefits:** Paid time off, 401k with company match, Health/Dental/Vision, HSA contributions.

Submit your resume at [www.senseeker.com](http://www.senseeker.com) or email it to [careers@senseeker.com](mailto:careers@senseeker.com)

*\*This position requires work on projects which are protected under US ITAR laws, consequently, **only US Persons will be considered for this position by law.** For this purpose, the term "US person" means an individual who is a citizen of the United States, a lawful permanent resident alien of the US (e.g., a "Green Card" holder), a refugee, or someone in the US as a protected political asylee, or under amnesty.*

Senseeker Corp is a wholly US owned transducer IC and cryogenic test solutions company. We specialize in the design of state-of-the-art digital readout integrated circuits, cryogenic test equipment, electronics and software. Senseeker's products and IP enable FPA developers to produce world class infrared image sensing solutions. We are located in beautiful Santa Barbara, CA, amid the west coast's hub of infrared and scientific image sensor companies. For more information see [www.senseeker.com](http://www.senseeker.com).

*Senseeker Corp is an equal opportunity employer and considers qualified applications for employment without regard to sexual orientation, gender identity, gender expression, race, color, sex, creed, religion, national origin, age, disability, veteran status, or any other protected class.*