

Job Title: Sensor Architect IC Design Engineer (Principal Level)

Job Description: Senseseeker Engineering is looking for a Sensor Architect IC Design Engineer to drive the top-down system design of advanced digital readout integrated circuits for imaging applications. The sensor architect will also be responsible for driving the bottom-up evolution of pixels with advancements in device scaling and customer detector technology. Duties will include feasibility analysis, specifications development, chip architecture development, chip and block level design, simulation and verification, Verilog/Verilog-A behavioral modeling, design validation, characterization and noise analysis, contributing novel ideas to the company's IP portfolio, interfacing with customers, keeping up with the state-of-the art and maintaining good written documentation. The candidate should be comfortable working on a broad range of topics in a relaxed small business environment.

Work Experience: 10+ years

Required Education: Master's degree in electrical engineering with emphasis in mixed-signal IC design.

Preferred Education: Master's degree or Ph.D. in electrical engineering with emphasis in image sensors.

Required Attributes:

- US person status **required** for this position, pursuant to the terms of government contracts with Senseseeker*
- Demonstrated project leadership experience from technical feasibility analysis to tapeout and design verification testing
- Experience with specifications development and image sensor architecture development
- Chip level design and verification of complex mixed-signal circuits
- Excellent teamwork skills, good written and verbal communication and a self-driven approach to work
- Organized and methodical design, layout and documentation
- Strong interest in integrated circuits and design techniques
- CMOS design, simulation and analysis of amplifiers, switched-capacitor circuits, converters and I/O
- Successful execution of high density mixed-signal layouts, parasitic extractions and layout improvements

Desired Attributes:

- Practical design experience with readout ICs
- Image sensor characterization and noise analysis
- Working knowledge of wafer level testing and yield analysis
- Comfortable with multiple IC design CAD tools and foundry PDKs
- Experience with Mentor Tanner / Calibre design flow

Job Type and Location: Full-time in Goleta, CA, USA

Benefits: Competitive compensation, growth opportunity, flexible schedule, relaxed working environment, located in a great beachside community

Send your resume to careers@senseseeker.com

* This position requires work on projects which are protected under US ITAR laws, consequently, *only US Persons will be considered for this position by law*. For this purpose, the term "US person" means an individual who is a citizen of the United States, a lawful permanent resident alien of the US (e.g., a "Green Card" holder), a refugee, or someone in the US as a protected political asylee, or under amnesty.

Senseseeker Engineering Inc. provides silicon IP, IC and image sensor design and systems engineering all targeted at the unique needs of the scientific and infrared image sensor community. We specialize in the design of digital imaging sensors and readout integrated circuits for hybrid image sensing arrays. We are located in the beautiful Santa Barbara, CA, area amid the west coast's hub of infrared and scientific image sensor companies. For more information see www.senseseeker.com.



Senseeker Engineering Inc. is an equal opportunity employer and considers qualified applications for employment without regard to sexual orientation, gender identity, gender expression, race, color, sex, creed, religion, national origin, age, disability, veteran status, or any other protected class.