

**Job Title:** CMOS IC Layout Engineer

**Job Description:** Senseseeker Engineering is looking for a CMOS integrated circuit layout engineer to join our design team and take on 65 nm – 180 nm CMOS mixed-signal layout and verification for our readout integrated circuits. Responsibilities will include initial chip floor planning, working closely with design engineers to provide critical feedback to designs and supply distribution schemes and understand the layout needs of each circuit. You will also help improve our EDA tool flow and efficiency. We expect you to take ownership of large arrayed complex layouts and perform layout, verification, and extractions on high density memory, pixels, converters, arrayed circuits, support circuitry and I/O pad rings. You should be comfortable working in a relaxed small business environment and understand how to get tasks accomplished independently when needed.

**Work Experience:** 5 - 25 years

**Required Education:** Associate's degree or equivalent with specialization in mask layout.

**Preferred Education:** Bachelor's degree in electrical engineering, physics or computer engineering with emphasis in layout, CMOS EDA and photomask design.

**Required Attributes:**

- US person status *required* for this position, pursuant to the terms of government contracts with Senseseeker\*
- Excellent teamwork skills, good written and verbal communication and a self-driven approach to work
- Organized and methodical
- Extensive knowledge of high density arrayed CMOS low-noise analog and mixed-signal layout techniques
- Proficient using Calibre LVS/DRC/PEX and ability to improve efficiency with EDA scripting
- Successful execution of high speed mixed-signal layouts, parasitic extractions and layout improvements

**Desired Attributes:**

- Practical layout experience with image sensors and digital readout ICs
- Proficient at advanced layout techniques including high frequency routing,
- Comfortable with multiple IC design CAD tools and foundry PDKs
- Experience with automation tools for digital layout and verification

**Job Type and Location:** Full-time in Goleta, CA, USA

**Benefits:** Competitive compensation, growth opportunity, flexible schedule, relaxed working environment, located in a great beachside community

Send your resume to [careers@senseseeker.com](mailto:careers@senseseeker.com)

\* This position requires work on projects which are protected under US ITAR laws, consequently, *only US Persons will be considered for this position by law*. For this purpose, the term "US person" means an individual who is a citizen of the United States, a lawful permanent resident alien of the US (e.g., a "Green Card" holder), a refugee, or someone in the US as a protected political asylee, or under amnesty.

Senseseeker Engineering Inc. provides silicon IP, IC and image sensor design and systems engineering all targeted at the unique needs of the scientific and infrared image sensor community. We specialize in the design of digital imaging sensors and readout integrated circuits for hybrid image sensing arrays. We are located in the beautiful Santa Barbara, CA, area amid the west coast's hub of infrared and scientific image sensor companies. For more information see [www.senseseeker.com](http://www.senseseeker.com).

*Senseseeker Engineering Inc. is an equal opportunity employer and considers qualified applications for employment without regard to sexual orientation, gender identity, gender expression, race, color, sex, creed, religion, national origin, age, disability, veteran status, or any other protected class.*